

IFW

App. Ser. No. 09/964,736  
Att'y Docket No. 2207/11983  
Assignee: Intel Corporation



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

Mark E. EIDSON

Serial No. 09/964,736

Filed: September 28, 2001

For: METHOD AND SYSTEM FOR  
ATTACHING MULTIPLE CLOCK  
SOURCES TO AN SDRAM ARRAY

Examiner: Nimesh G. PATEL

Art Unit 2112

**RESPONSE TO OFFICE ACTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed April 15, 2004 in the above-identified application, the Applicant respectfully submits the following.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 6 of this paper.